

Assignment Brief: Comp3402 Nature of Computing 2023-24 Sem 1&2

Assignment 2 (Part2)	Position Paper 2 (Part 2)
Word Limit or equivalent (e.g. time)	No word limit. Indicative time 14 hours
Weighting	15%
Learning Outcomes Assessed	(5) Critically reflect on the design of digital and analogue computers
Submission date	23 th February 2024 15:00
Feedback date	20 days following the respective submission date
Module Leader	Dr. Colin Price c.price@worc.ac.uk
Verified by	Bradley Carwardine

If anything about this assignment is not clear to you, please contact your module leader.

What do I need to do to make a success of this assignment?	<p>You should write a position paper stating your position on the following statement,</p> <p style="text-align: center;">“There are clear design principles for digital computers.”</p> <p>You draw on your worksheet material and focus on explaining how digital computers are designed, supported by material not directly taught in class.</p>
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How should I present my work?	<p>Your paper should contain three sections: (1) A short introduction / abstract where you tell the reader what to expect reading your paper, (2) The main body of your paper, (3) A conclusion where you state your position.</p> <p>The main body of your paper should contain two sections. The first section is based on synthesis of digital computer components using VHDL. The second section records either your advanced work (Option 1) or your ‘research’ approach (Option 2). The latter must include citations of journal articles.</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 33%; text-align: center;">Section 1</th> <th colspan="2" style="text-align: center;">Section 2</th> </tr> <tr> <th></th> <th style="text-align: center;">Option 1</th> <th style="text-align: center;">Option 2</th> </tr> </thead> <tbody> <tr> <td style="vertical-align: top;">You will investigate some digital building blocks for CPUs such as the full adder, using VHDL.</td> <td style="vertical-align: top;">You will apply your understanding of VHDL to a challenging problem, such as the synthesis of an entire CPU.</td> <td style="vertical-align: top;">Review the literature related to digital CPU architectures and provide a critical comparison.</td> </tr> </tbody> </table>	Section 1	Section 2			Option 1	Option 2	You will investigate some digital building blocks for CPUs such as the full adder, using VHDL.	You will apply your understanding of VHDL to a challenging problem, such as the synthesis of an entire CPU.	Review the literature related to digital CPU architectures and provide a critical comparison.
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<p>How can I obtain guidance on my assignment?</p>	<p>You can show your tutor your work in progress at any time no later than one week before the submission deadline to enable you to review and address feedback provided to develop your work.</p> <p>The assignment briefing will be given w/c 22th January 2024. You may ask for any additional support or guidance in class or via email c.price@worc.ac.uk</p>
<p>How and when do I hand my assignment in?</p>	<p>Your work must be word-processed/typed and should clearly show your student number. You should submit your work by the 3pm deadlines indicated above. You should submit your work to Blackboard which is available via <u>MyDay</u>. You are required to keep a copy of work handed in.</p> <p>See the separate Assignment Support Information document on Blackboard for help on how to submit or what to do if you are having trouble submitting your assignment.</p>
<p>How will my assignment be marked?</p>	<p>Specific marking criteria for your assignment is provided in the Grading Matrix within this document.</p> <p>You are strongly advised to check your completed work against the Grading Matrix to ensure have completed all areas required before you submit it.</p> <p>You should also ensure you adhere to the word limit / word count stated in your assessment brief document, details of which can be found in the University's Assessment Policy http://www.worc.ac.uk/aqu/documents/AssessmentPolicy.pdf</p>

L6 Grading Matrix for Comp3402 Position Paper 2 (Part 2)

This matrix captures the assessment criteria for this part of the coursework.

Student Name/Number:		Assignment No: 2	Weighting: 15%
Module Code:	Comp3402	Assignment Title: PP2 (part2)	
Module Title:	Nature of Computing	Semester: 1&2	
Learning Outcomes being assessed: <i>LO5. Critically reflect on the design of digital or analogue computers</i>			

To best understand this matrix, start by reading the 'baseline' grade C

	Knowledge and understanding	Autonomy in Learning		Communication
		Option 1	Option 2	
	Investigation of Digital Building Blocks	Application of VHDL to a challenging problem	Critical comparison of digital CPU architectures	Well-written Paper
	60	30		10
A	Explanation of the synthesis of both a combinatorial AND a sequential digital block.	Solution is explained in detail WITH critical analysis.	Detailed material AND critical evaluation.	Position is coherent and persuasive.
B	Detailed explanation of the synthesis of a single digital block.	Solution is explained in detail.	Material is detailed	Position is coherent.
C	Synthesis and explanation of either a single digital block e.g., full adder.	Correct solution is presented and explained.	Material presented has been carefully selected,	Statement of position in clear language.
D	Synthesis may contain un-corrected errors. Explanation lacking in depth.	Solution may contain errors. Explanation lacking in depth.	Some material presented though is not particularly relevant	Statement of position is too short or unclear.
F	Little or no attempt at synthesis or completely erroneous synthesis.	Little or no attempt at presenting a solution.	Little or no attempt to present material.	Little or no statement of position.

My approach to supporting and assessing SPaG on this assignment will appear on the Module Webpage and will be explained during the introductory session 25th September 2023.

RESULTS ARE PROVISIONAL UNTIL AGREED BY THE BOARD OF EXAMINERS

