

<b>Assignment 2 (Part2)</b>	Position Paper 2 (Part 2)
<b>Word Limit or equivalent (e.g. time)</b>	No word limit. Indicative time 14 hours
<b>Weighting</b>	15%
<b>Learning Outcomes Assessed</b>	(5) Critically reflect on the design of digital and analogue computers
<b>Submission date</b>	13 <sup>th</sup> February 2023 15:00
<b>Feedback date</b>	20 days following the respective submission date
<b>Module Leader</b>	Dr. Colin Price <a href="mailto:c.price@worc.ac.uk">c.price@worc.ac.uk</a>
<b>Verified by</b>	Dr. Marc Price

*If anything about this assignment is not clear to you, please contact your module leader.*

<b>What do I need to do to make a success of this assignment?</b>	<p>You should write a position paper stating your position on the following statement</p> <p style="text-align: center;"><b>“There are clear design principles for both analogue and digital computers”</b></p> <p>You draw on your worksheet material and focus on explaining how both digital and analogue computers are designed, supported by material not seen in class.</p>
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<b>How should I present my work?</b>	<p>Your paper should contain three sections: (1) A short <b>introduction / abstract</b> where you tell the reader what to expect reading your paper, (2) The <b>main body</b> of your paper, (3) A conclusion where you <b>state your position</b>.</p> <p>The main body of your paper should contain two sections. The first section is based on synthesis of digital computer components using VHDL, <b>or</b> synthesis of a simulated analogue computer using e.g., Octave. The second section records either your advanced work <b>or</b> your ‘research’ approach. The latter must include citations of journal articles.</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 33%; text-align: center;">Section 1</th> <th colspan="2" style="text-align: center;">Section 2</th> </tr> <tr> <td></td> <th style="width: 33%; text-align: center;">Option 1</th> <th style="width: 33%; text-align: center;">Option 2</th> </tr> </thead> <tbody> <tr> <td style="vertical-align: top;"> <p>You will investigate some digital building blocks for CPUs such as the full adder, using VHDL.</p> <p style="text-align: center;"><b>OR</b></p> <p>You will investigate the design of an analogue computer to solve a straightforward problem, e.g., car suspension.</p> </td> <td style="vertical-align: top;"> <p>You will apply your understanding of VHDL to a challenging problem, such as the synthesis of an entire CPU.</p> <p style="text-align: center;"><b>OR</b></p> <p>You will apply your understanding of analogue computers to a challenging control problem, such as the Segway</p> </td> <td style="vertical-align: top;"> <p>Review the literature related to digital CPU architectures and provide a critical comparison.</p> <p style="text-align: center;"><b>OR</b></p> <p>Present a historical development of analogue computer applications.</p> </td> </tr> </tbody> </table>	Section 1	Section 2			Option 1	Option 2	<p>You will investigate some digital building blocks for CPUs such as the full adder, using VHDL.</p> <p style="text-align: center;"><b>OR</b></p> <p>You will investigate the design of an analogue computer to solve a straightforward problem, e.g., car suspension.</p>	<p>You will apply your understanding of VHDL to a challenging problem, such as the synthesis of an entire CPU.</p> <p style="text-align: center;"><b>OR</b></p> <p>You will apply your understanding of analogue computers to a challenging control problem, such as the Segway</p>	<p>Review the literature related to digital CPU architectures and provide a critical comparison.</p> <p style="text-align: center;"><b>OR</b></p> <p>Present a historical development of analogue computer applications.</p>
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<p><b>How can I obtain guidance on my assignment?</b></p>	<p>You can show your tutor your work in progress at any time no later than one week before the submission deadline to enable you to review and address feedback provided to develop your work.</p> <p>The assignment briefing will be given on 16<sup>th</sup> January 2023. You may ask for any additional support or guidance in class or via email <a href="mailto:c.price@worc.ac.uk">c.price@worc.ac.uk</a></p>
<p><b>How and when do I hand my assignment in?</b></p>	<p>Your work must be word-processed/typed and should clearly show your student number. You should submit your work by the 3pm deadlines indicated above. You should submit your work to Blackboard which is available via <u>MyDay</u>. <b>You are required to keep a copy of work handed in.</b></p> <p>See the separate <b>Assignment Support Information</b> document on Blackboard for help on how to submit or what to do if you are having trouble submitting your assignment.</p>
<p><b>How will my assignment be marked?</b></p>	<p>Specific marking criteria for your assignment is provided in the Grading Matrix within this document.</p> <p>You are strongly advised to check your completed work against the Grading Matrix to ensure have completed all areas required before you submit it.</p> <p>You should also ensure you adhere to the word limit / word count stated in your assessment brief document, details of which can be found in the University's Assessment Policy <a href="http://www.worc.ac.uk/aqu/documents/AssessmentPolicy.pdf">http://www.worc.ac.uk/aqu/documents/AssessmentPolicy.pdf</a></p>

## L6 Grading Matrix for Comp3402 Position Paper 2 (Part 2)

This matrix captures the assessment criteria for this part of the coursework.

<b>Student Number/Name:</b>	<b>Academic Year and Semester:</b> 2022-23 AS	<i>(5) Critically reflect on the design of digital and analogue computers</i>
<b>Module Code / Title:</b> Comp3402 Nature of Computing	<b>Assignment No/Weighting:</b> Ass 2 (part 2). Weighting 15%	
	<b>Assessment Title:</b> Position Paper 2 (Part 2)	

*To best understand this matrix, start by reading the 'baseline' grade C*

	Knowledge and understanding	Autonomy in Learning		Communication
		Option 1	Option 2	
	Investigation of Digital Building Blocks OR analogue computer design	Application of VHDL OR analogue computer design to a challenging problem	Critical comparison of digital CPU architectures OR Historical development of analogue computer applications	Well-written Paper
	<b>60</b>	<b>30</b>		<b>10</b>
<b>A</b>	Explanation of the synthesis of both a combinatorial and sequential block <b>OR</b> a non-linear analogue computer problem	Solution is explained in detail <b>WITH</b> critical analysis.	Detailed material <b>AND</b> critical evaluation.	Position is coherent and persuasive.
<b>B</b>	Detailed explanation of the synthesis of a single digital block <b>OR</b> a linear analogue computer problem.	Solution is explained in detail.	Material is detailed	Position is coherent.
<b>C</b>	Synthesis and explanation of either a single digital block e.g., full adder <b>OR</b> an analogue computer solution to a linear problem.	Correct solution is presented and explained.	Material presented has been carefully selected,	Statement of position in clear language.
<b>D</b>	Synthesis may contain un-corrected errors. Explanation lacking in depth.	Solution may contain errors. Explanation lacking in depth.	Some material presented though is not particularly relevant	Statement of position is too short or unclear.
<b>F</b>	Little or no attempt at synthesis or completely erroneous synthesis.	Little or no attempt at presenting a solution.	Little or no attempt to present material.	Little or no statement of position.

My approach to supporting and assessing SPaG on this assignment will appear on the Module Webpage and will be explained during the introductory session 19<sup>th</sup> September 2022.

**Feedback on your assignment.**

Please review this feedback and use it to develop your work in your next assignment in this and your other modules. If anything is unclear, please ask the marker.

<b>Aspects done well and why:</b>		
<b>Aspects for improvement and why:</b>		
<b>How successful completion of this assignment helps your employability and achievement of graduate attributes:</b>		
See module outline for details of: (i) Reflective and resilient lifelong learning, (ii) Problem solving, (iii) Teamwork and effective communication, (iv) Digital citizenship.		
<b>Grade awarded:</b>		<b>Marker: Colin Price</b>
		<b>Moderator*: Marc Price</b>

*\* This person is responsible for moderating a sample of student work for this module. Your work may, or may not, have been included in this sample.*

I do not want my work to be used anonymously to help future students

**RESULTS ARE PROVISIONAL UNTIL AGREED BY THE BOARD OF EXAMINERS**

